



10EC82

Eighth Semester B.E. Degree Examination, Feb./Mar. 2022 Digital Switching Systems

Time: 3 hrs.

a.

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Max. Marks:100

Note: Answer any FIVE full questions, selecting at least TWO full questions from each part.

$\underline{PART - A}$ With a neat diagram, explain the hierarchy used in the national Public Switched Telephone

- Network (PSTN). (08 Marks) Explain the 3D-channel PCM primary multiplex, with a neat diagram. (08 Marks) b. c. A four wire circuit has an overall loss (Two-wire to two-wire) of 1db and the balance return loss at each end is 6db. Find: i) The singing point ii) The stability margin iii) The attenuation of talker and listener echo. (04 Marks) Differentiate between circuit switching and message switching. 2 (06 Marks) a. With a neat block diagram, explain subcriber's line interface circuit for a digital switch. b. (06 Marks) Explain the basic functions of a switching systems. (08 Marks) C. Define the following terms: i) Traffic intensity ii) Grade of service 3 a. iii) Busy hour iv) Holding time. (04 Marks) Derive an expression for the second Erlang's distribution starting from basic principles. b. (12 Marks) On an average, one call arrives every 5 seconds. During a period of 10 seconds. What is the C. probability that No call arrives i) ii) One call arrives iii) Two call arrives More than two calls arrive. iv) (04 Marks) What is Grading? Explain types of grading. 4 a. (08 Marks) Explain two stage switching network with necessary equations. b. (08 Marks) Design a three-stage network for 100 incoming trunks and 400 outgoing trunks. (04 Marks) c. PART – B 5 Explain the principle operation of T-S-T network and S-T-S network. With neat diagram. a. (12 Marks) Explain frame alignment of PCM signals in digital exchange. (08 Marks) b. Explain the basic software architecture of a typical digital switching system. 6 a. (10 Marks) Wire a short notes on a basic call model. (10 Marks) b. Write a short notes on the interfaces of a typical digital switching system central office. 7 a. (10 Marks) Explain the methodology used for reporting and correcting of field problems. (10 Marks) b 8
 - a. Write a short note on digital switching system hardware architecture. (10 Marks)
 b. Explain the three level scheme of recovery strategy in a digital switch. (10 Marks)

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